

FIG. 1

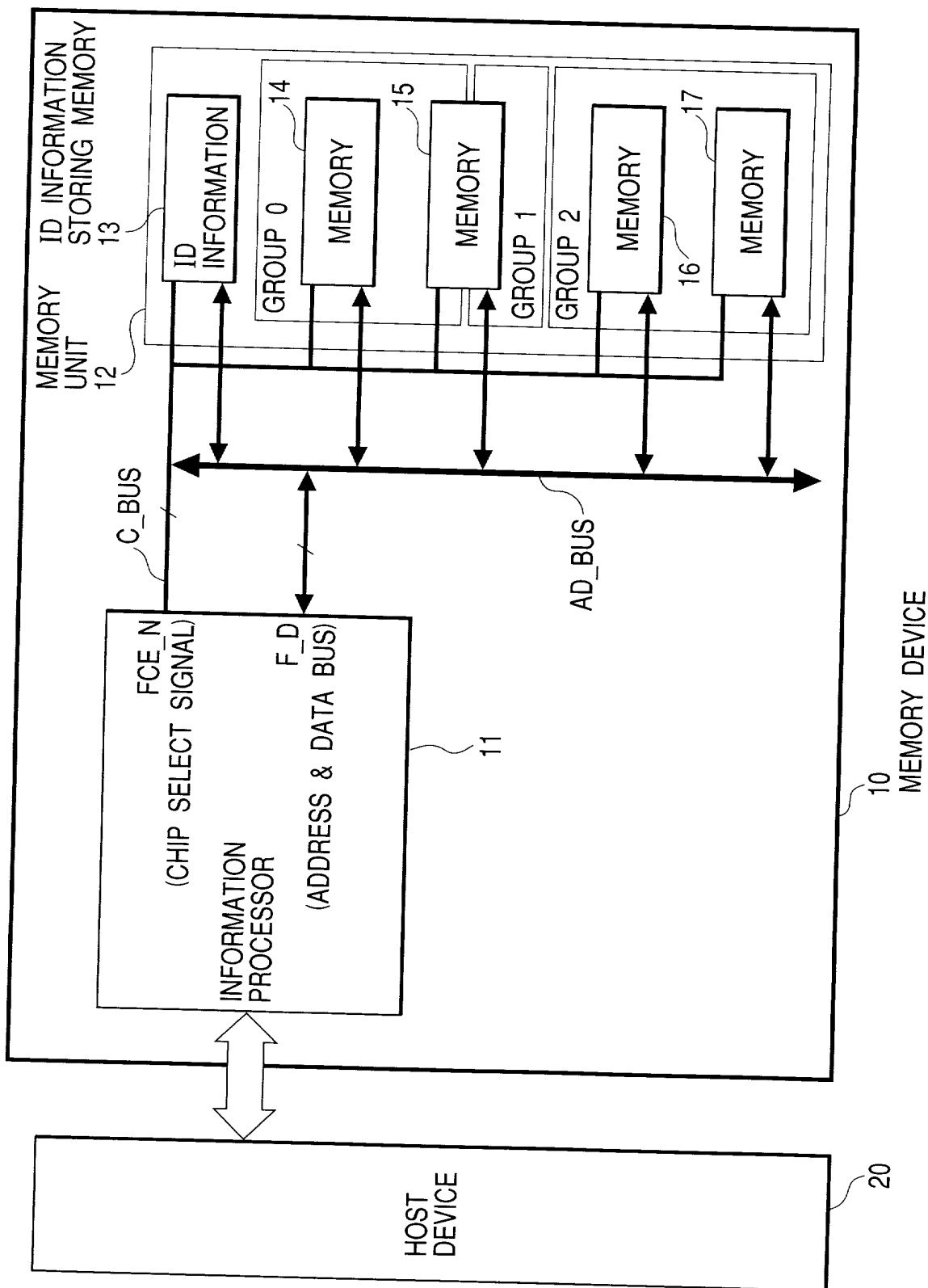


FIG. 2

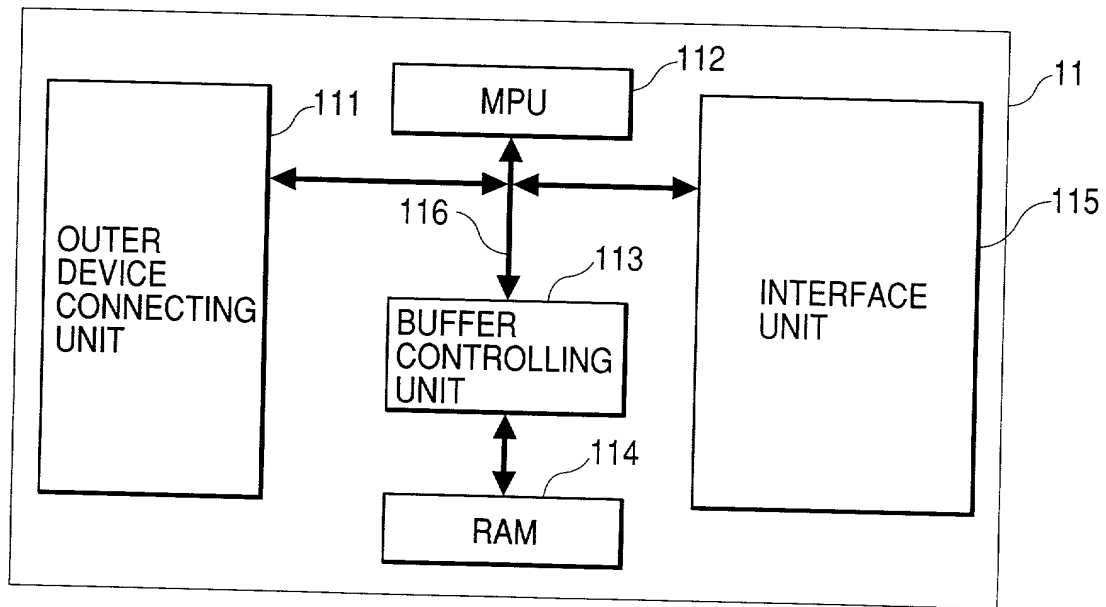


FIG. 3(A)

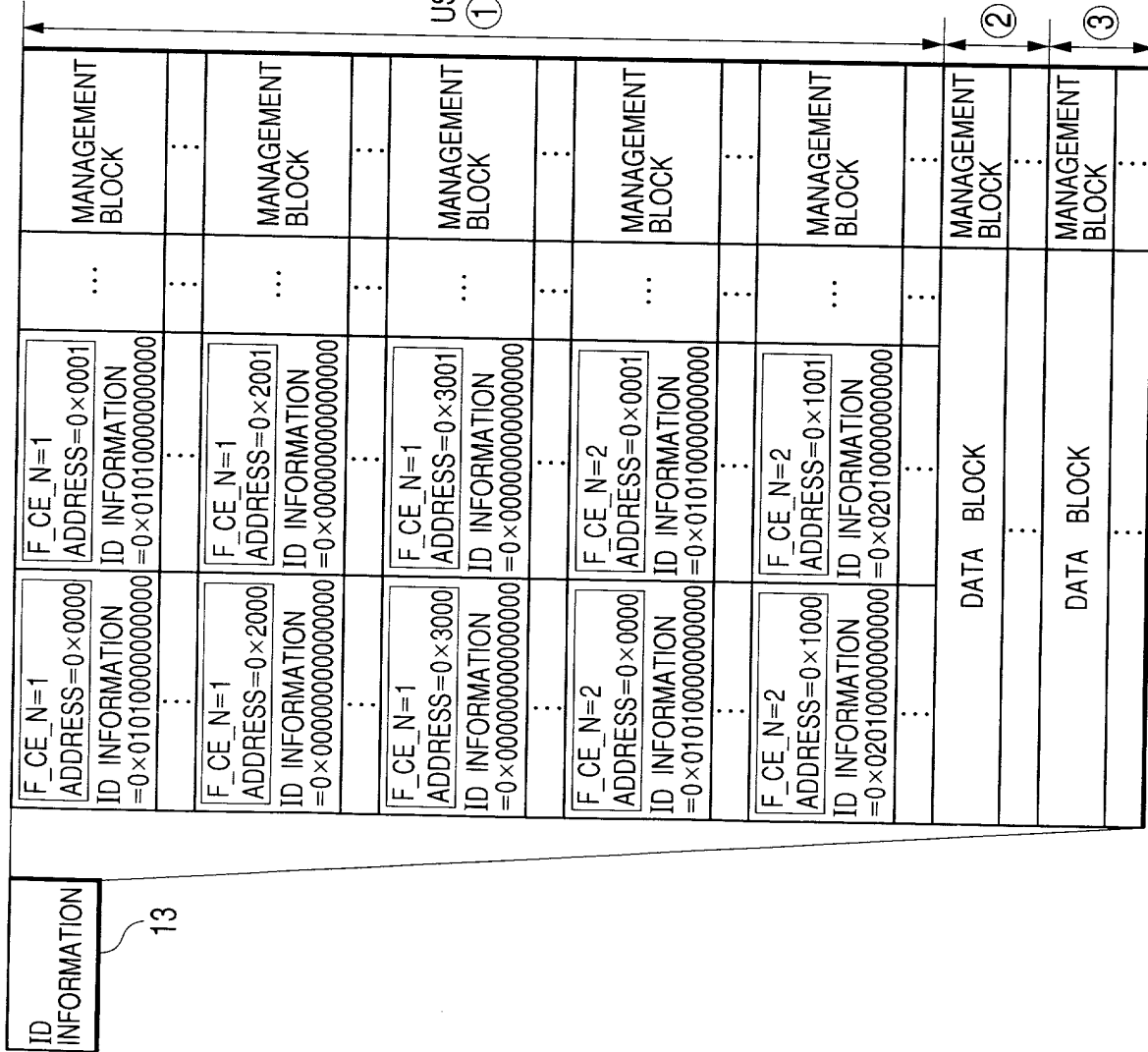


FIG. 3(B)

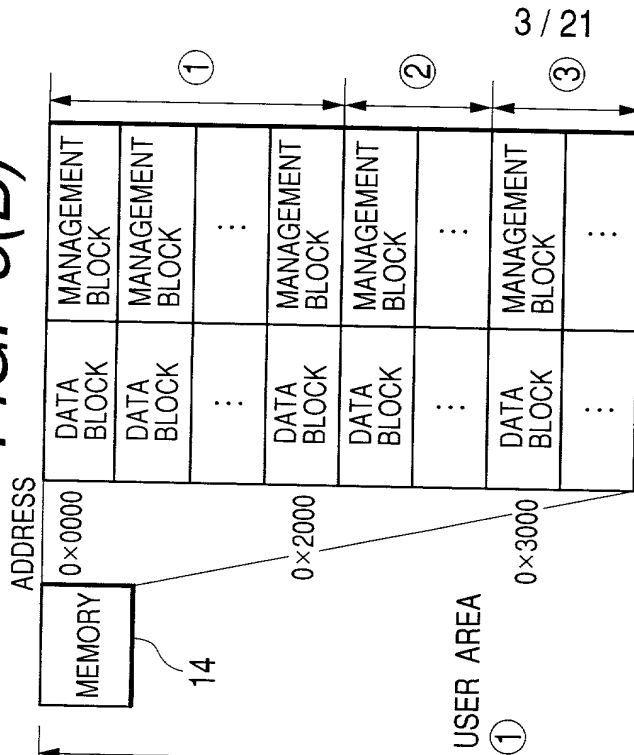
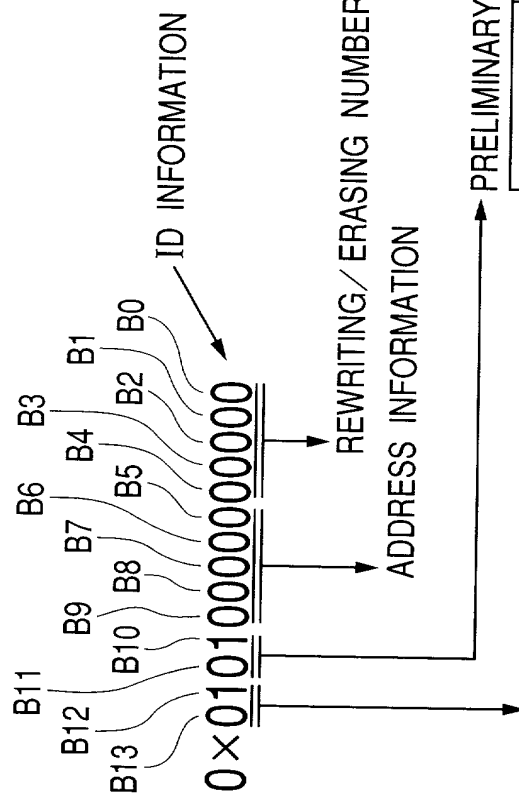


FIG. 4

	F_CE_N	ADDRESS (F_D)	ID INFORMATION	
GROUP 0	1	0×0000	0×01010000000000	NORMAL READ/WRITE
	⋮	⋮	⋮	
	1	0×1FFF	0×01010000000000	
	2	0×0000	0×01010000000000	
	⋮	⋮	⋮	
GROUP 1	2	0×0FFF	0×01010000000000	READ ONLY
	2	0×1000	0×02010000000000	
	⋮	⋮	⋮	
	2	0×1FFF	0×02010000000000	
GROUP 2	3	0×0000	0×04001000000000	MIRRORING (TIME DIFFERENCE)
	⋮	⋮	⋮	
	4	0×1FFF	0×040021FFF00000	

FIG. 5



PRELIMINARY AREA INFORMATION

(bit)	MIRRORING (TIME DIFFERENCE)	MIRRORING (SIMULTANEOUS)	SPARE PRIORITY	SPARE PROCESSING
0000_0000	NO	NO	NO	—
0000_0001	YES	NO	NO	—
0000_0010	NO	YES	NO	—
0000_0100	NO	NO	YES	—
0000_0111	YES	YES	NO	—
0000_1111	YES	YES	YES	—
0000_0110	NO	YES	NO	—
0000_1110	NO	YES	YES	—
:	:	:	:	:
1111_xxxx	—	—	—	FINISH

CONTROLLING INFORMATION

(bit)	CONTROLLING CONTENTS
0000_0001	NORMAL READ/ WRITE AREA
0000_0010	READ ONLY (WRITE PROTECT) AREA
0000_0011	WRITE ONLY AREA
0000_0100	MIRRORING (TIME DIFFERENCE) AREA
0000_1000	MIRRORING (SIMULTANEOUS) AREA
1111_xxxx	SPARE AREA
:	:

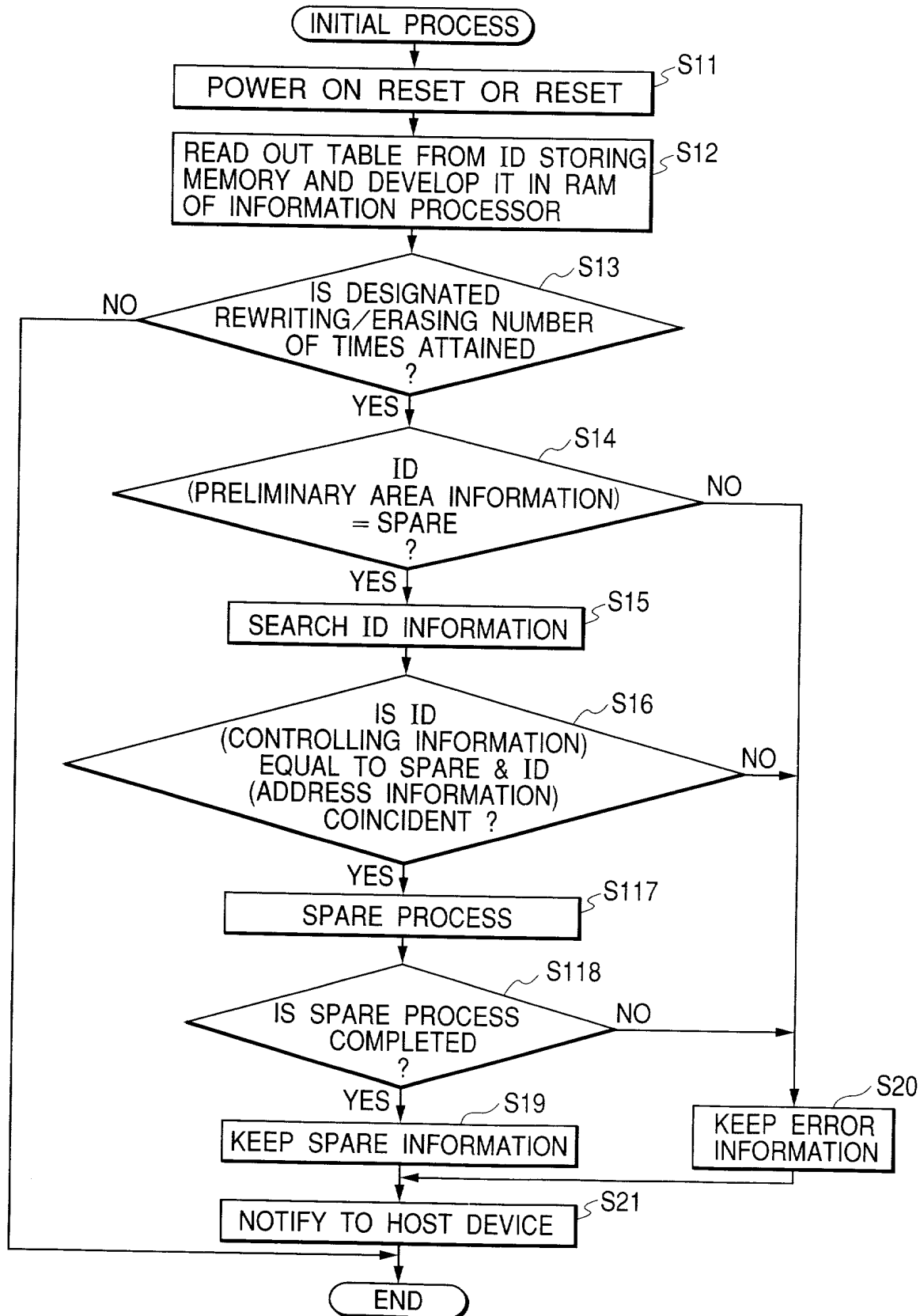
FIG. 6

FIG. 7

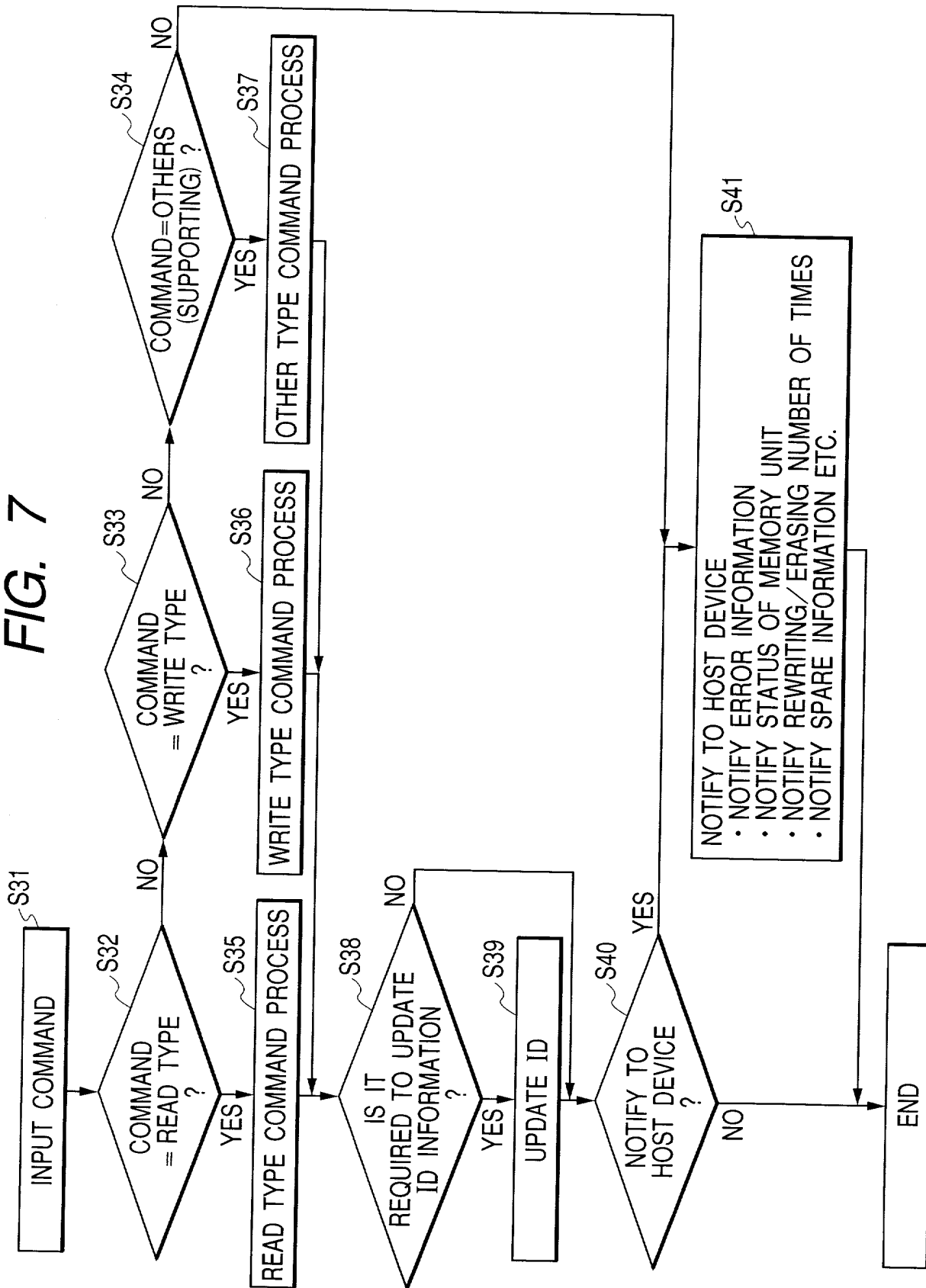
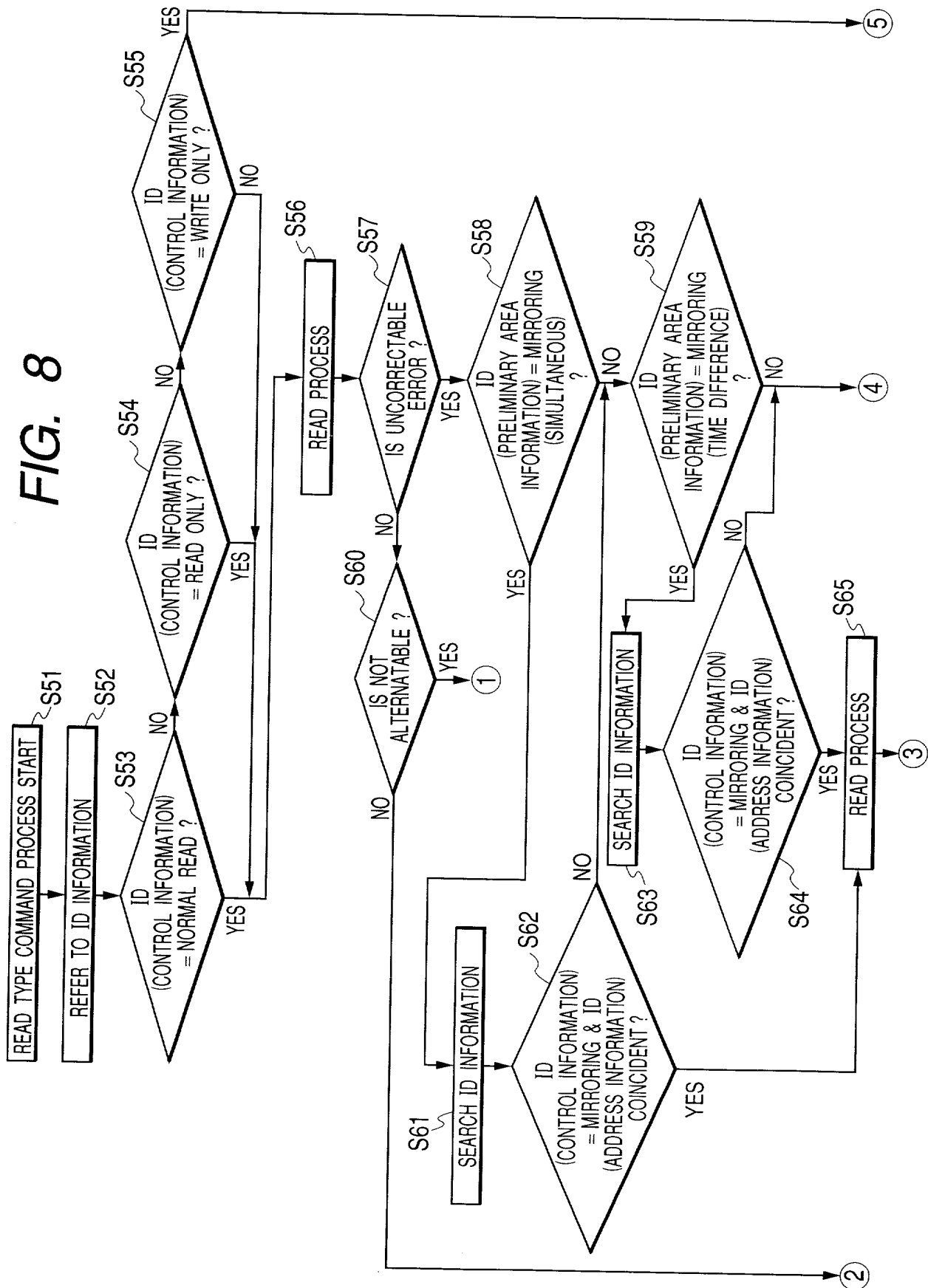


FIG. 8



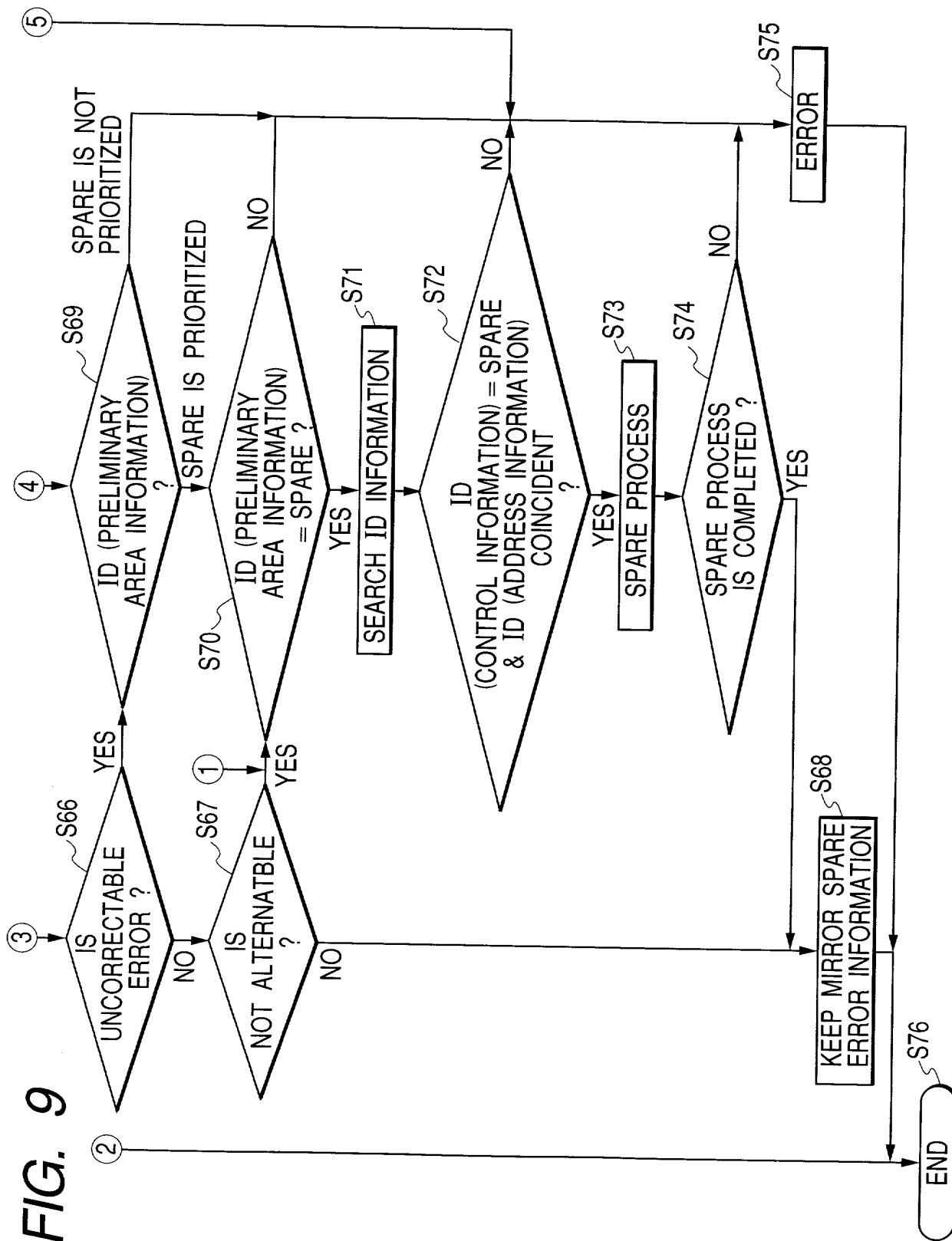


FIG. 10

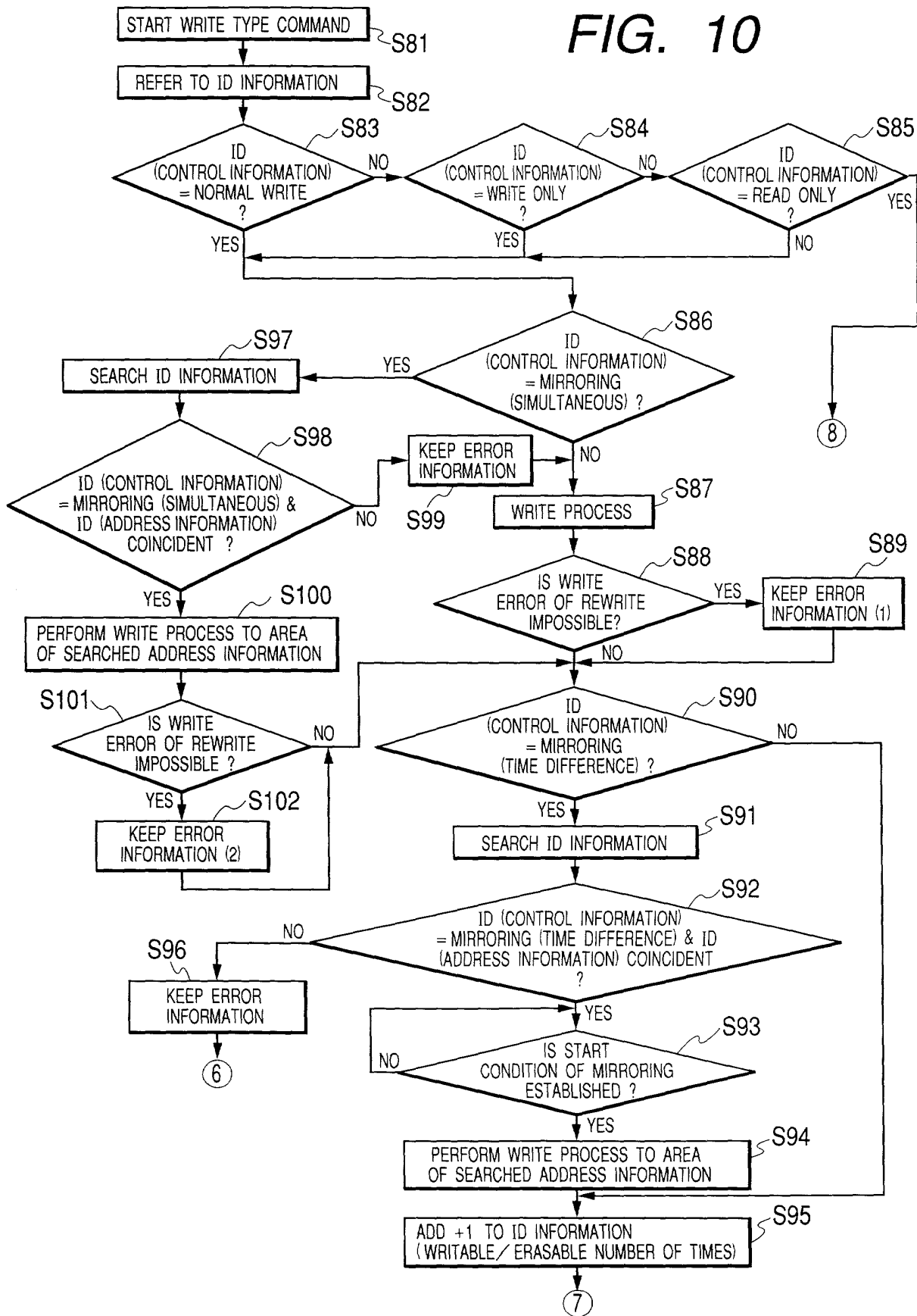


FIG. 11

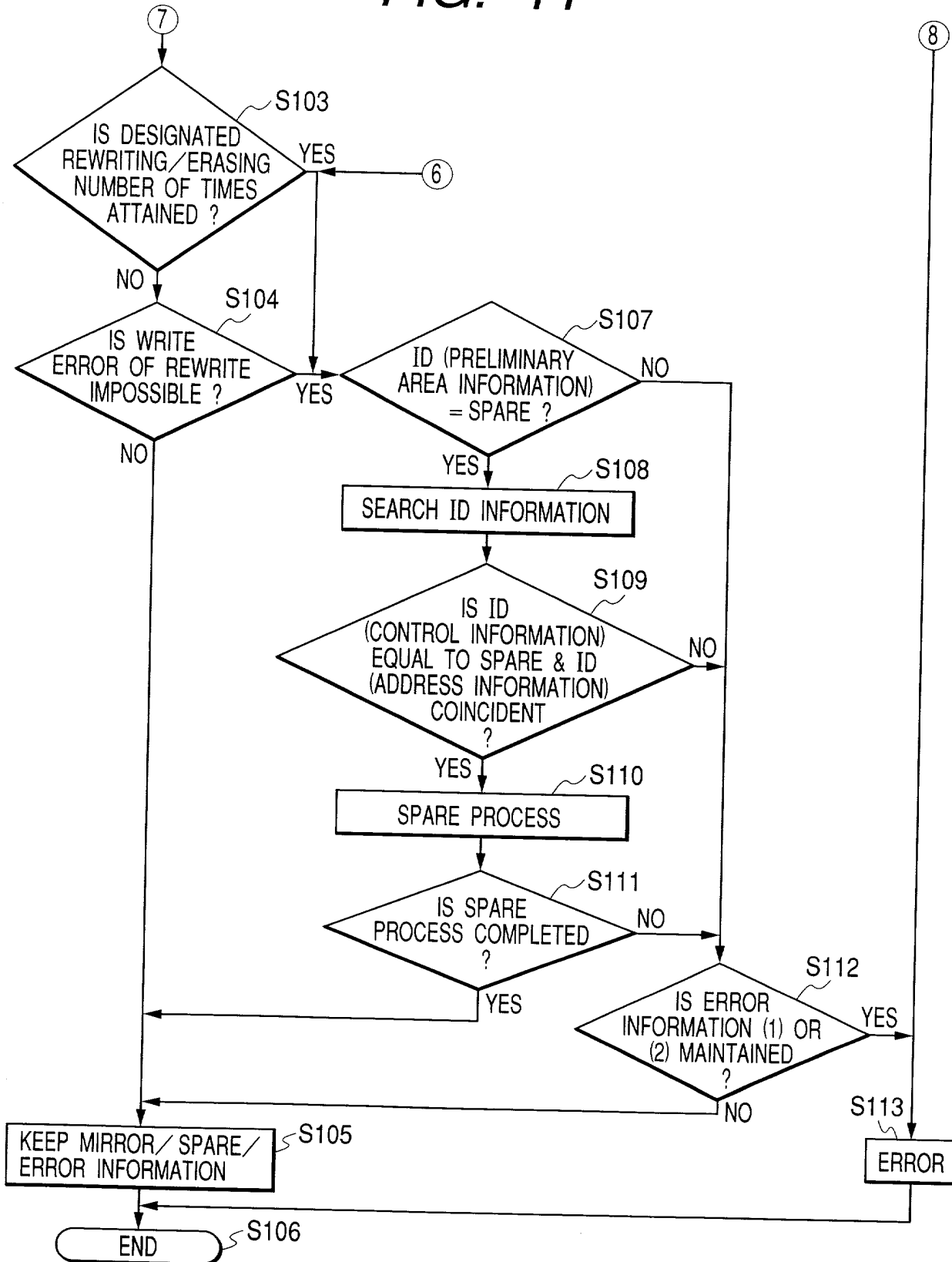


FIG. 12

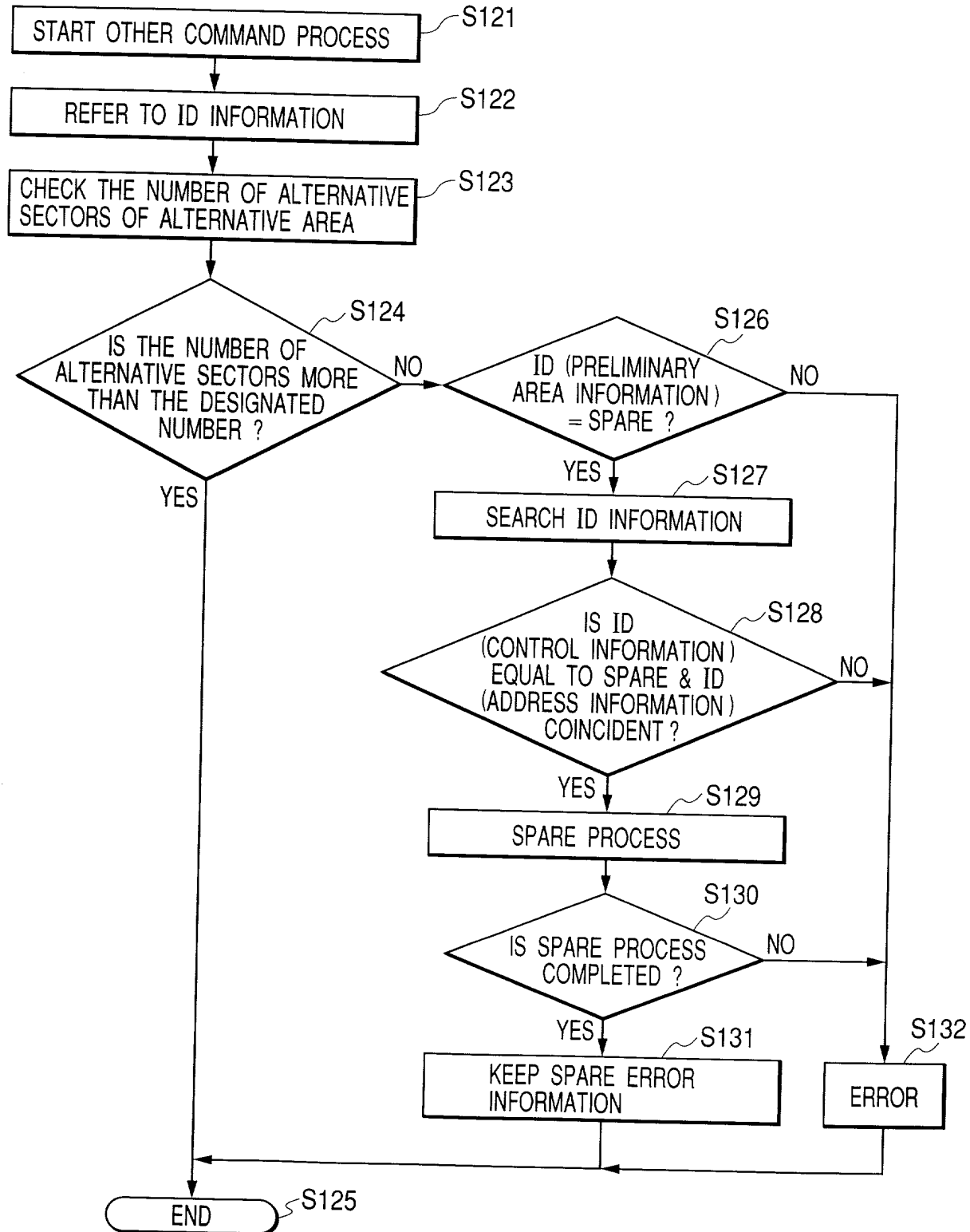


FIG. 13

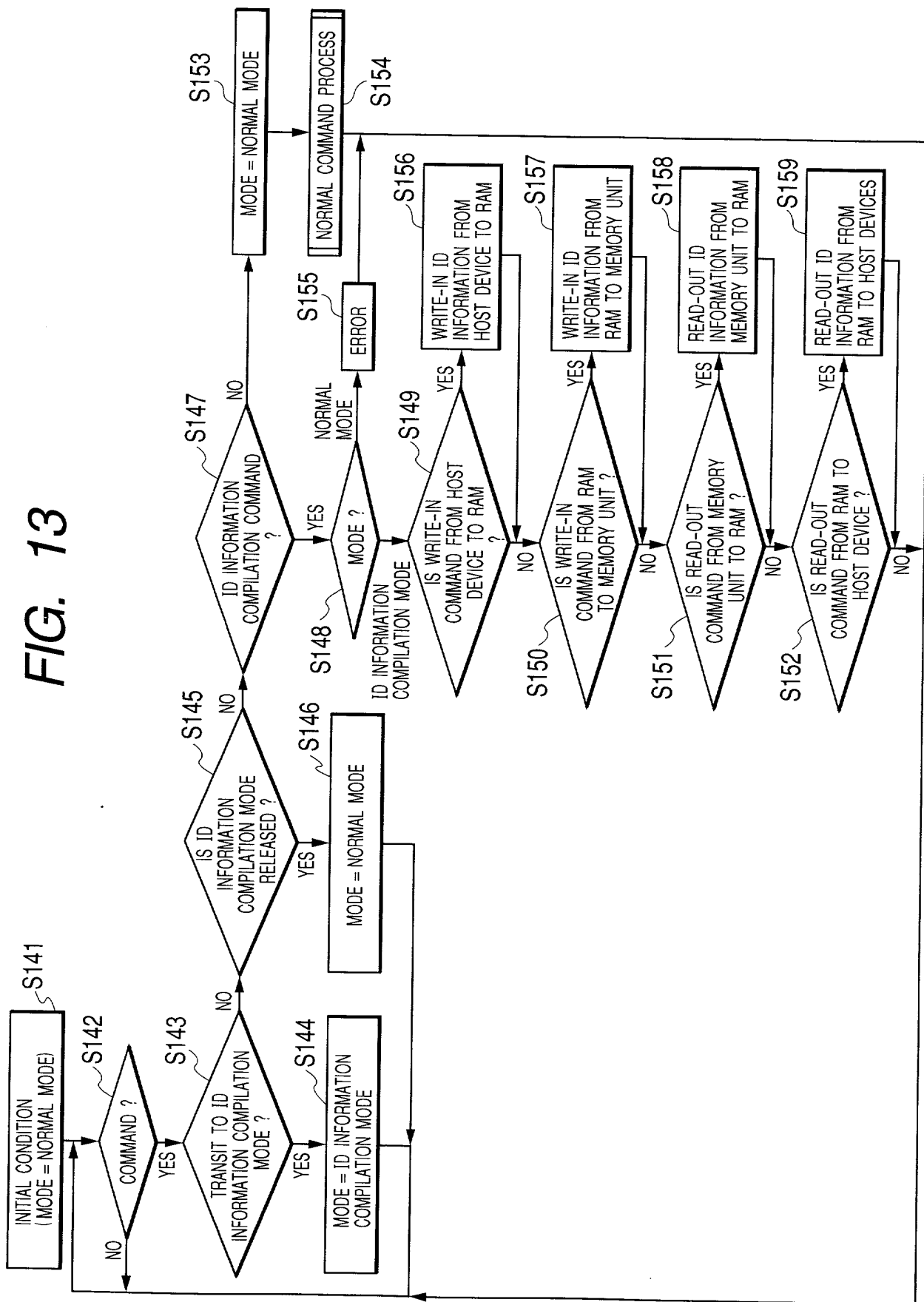


FIG. 14

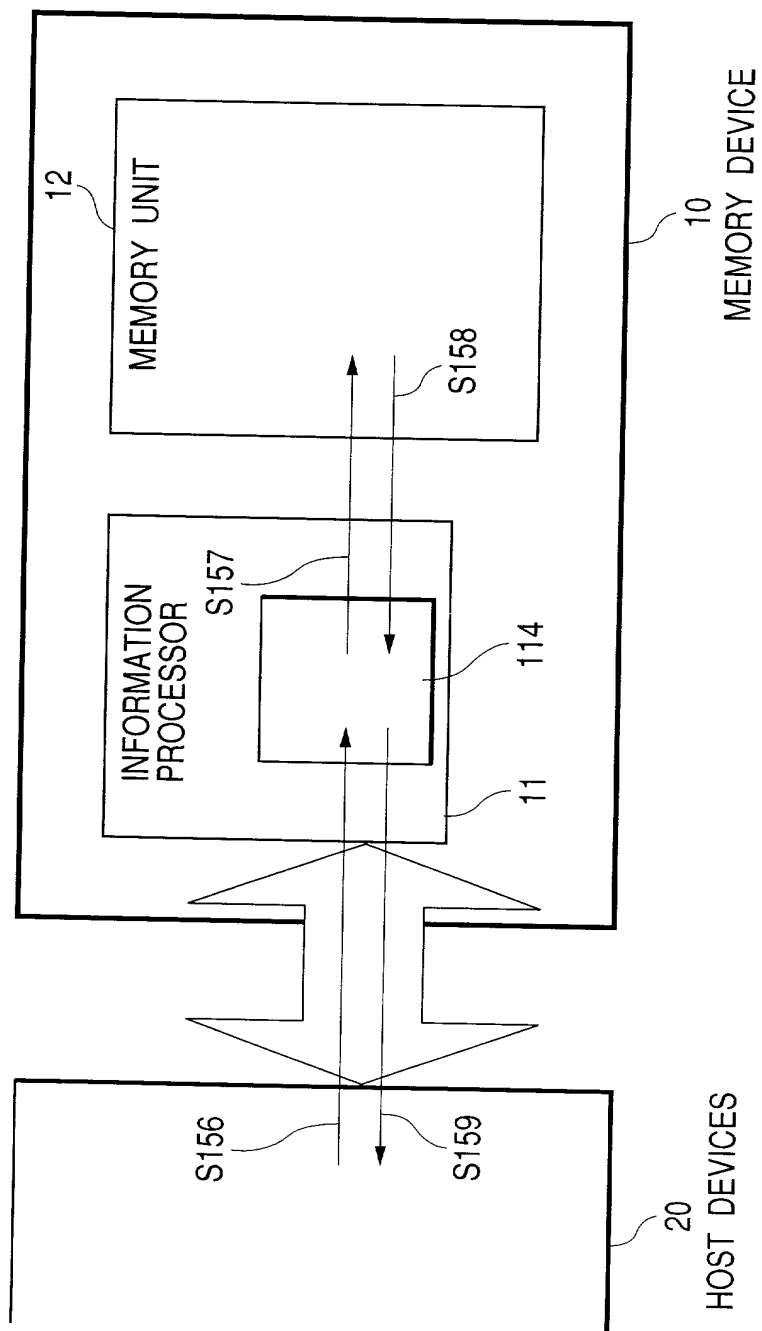


FIG. 15

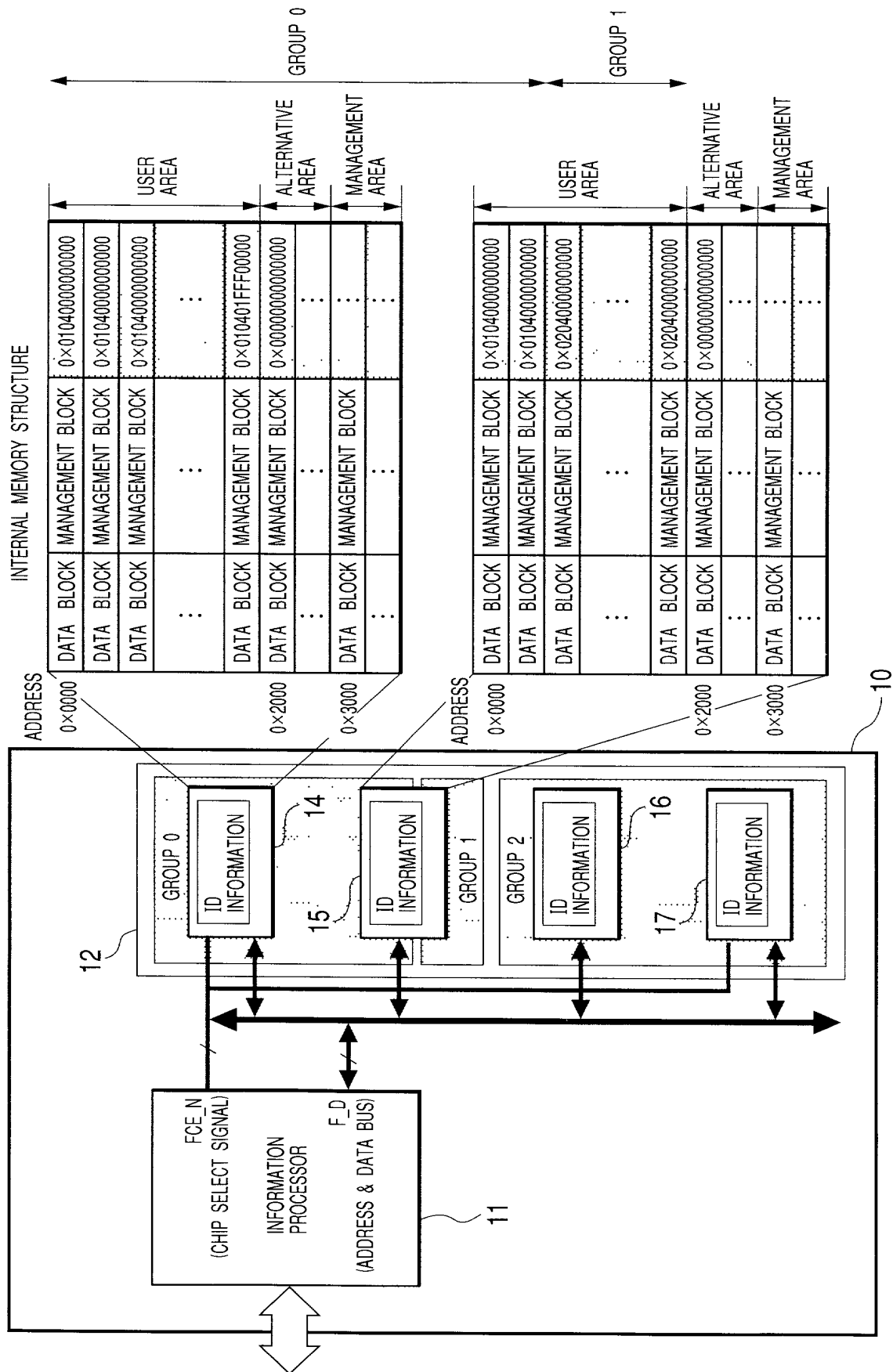


FIG. 16

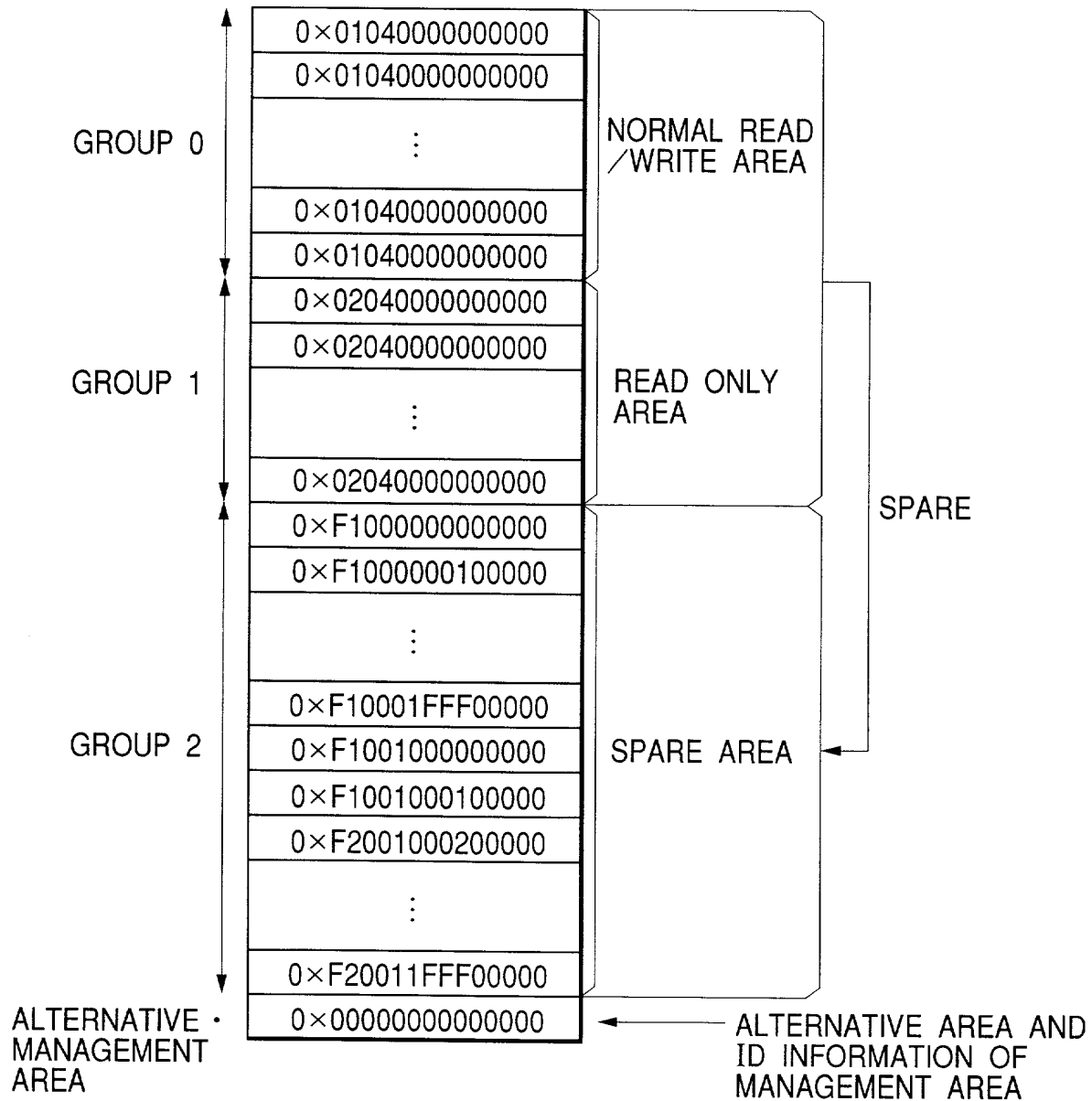


FIG. 17

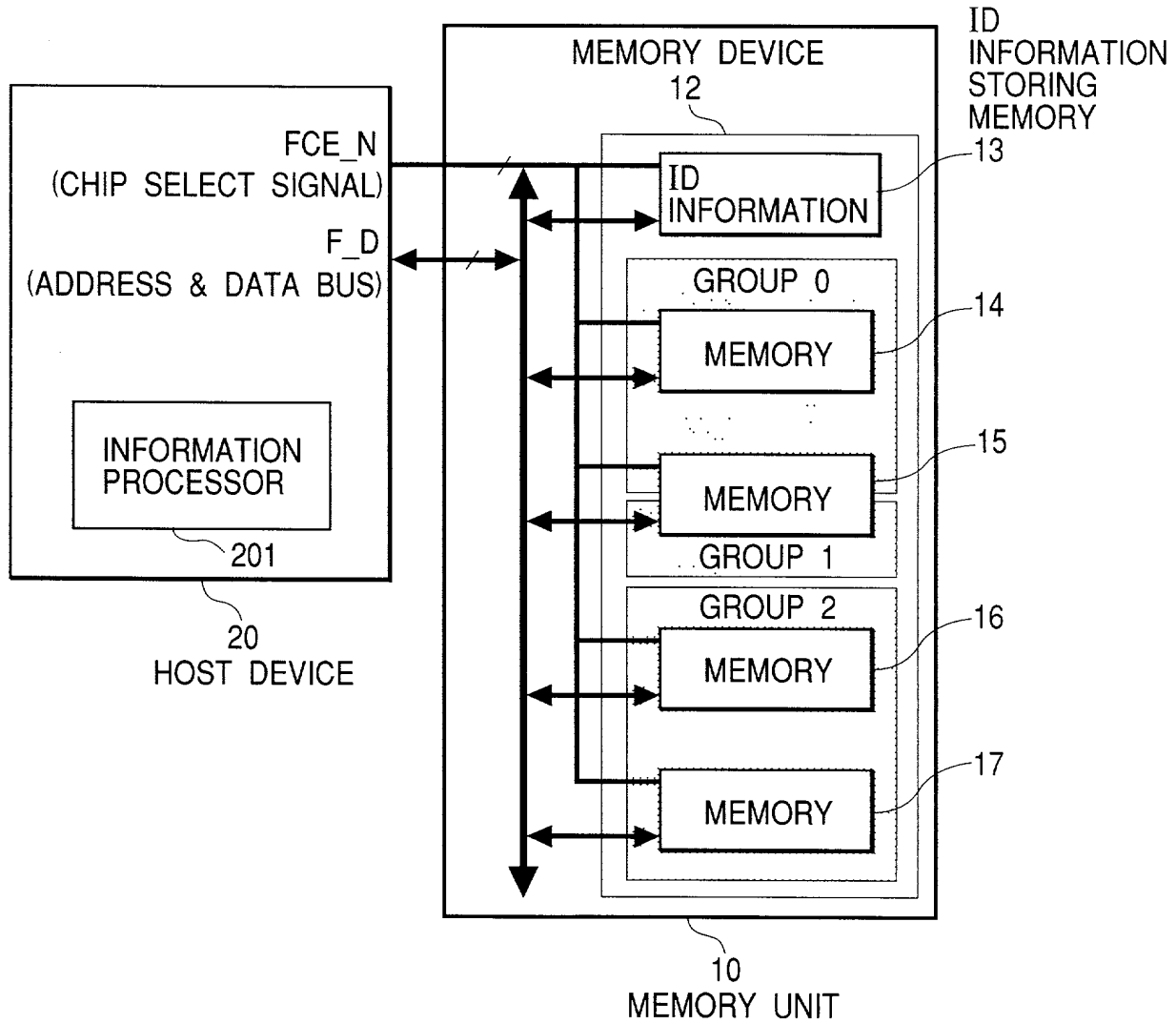


FIG. 18

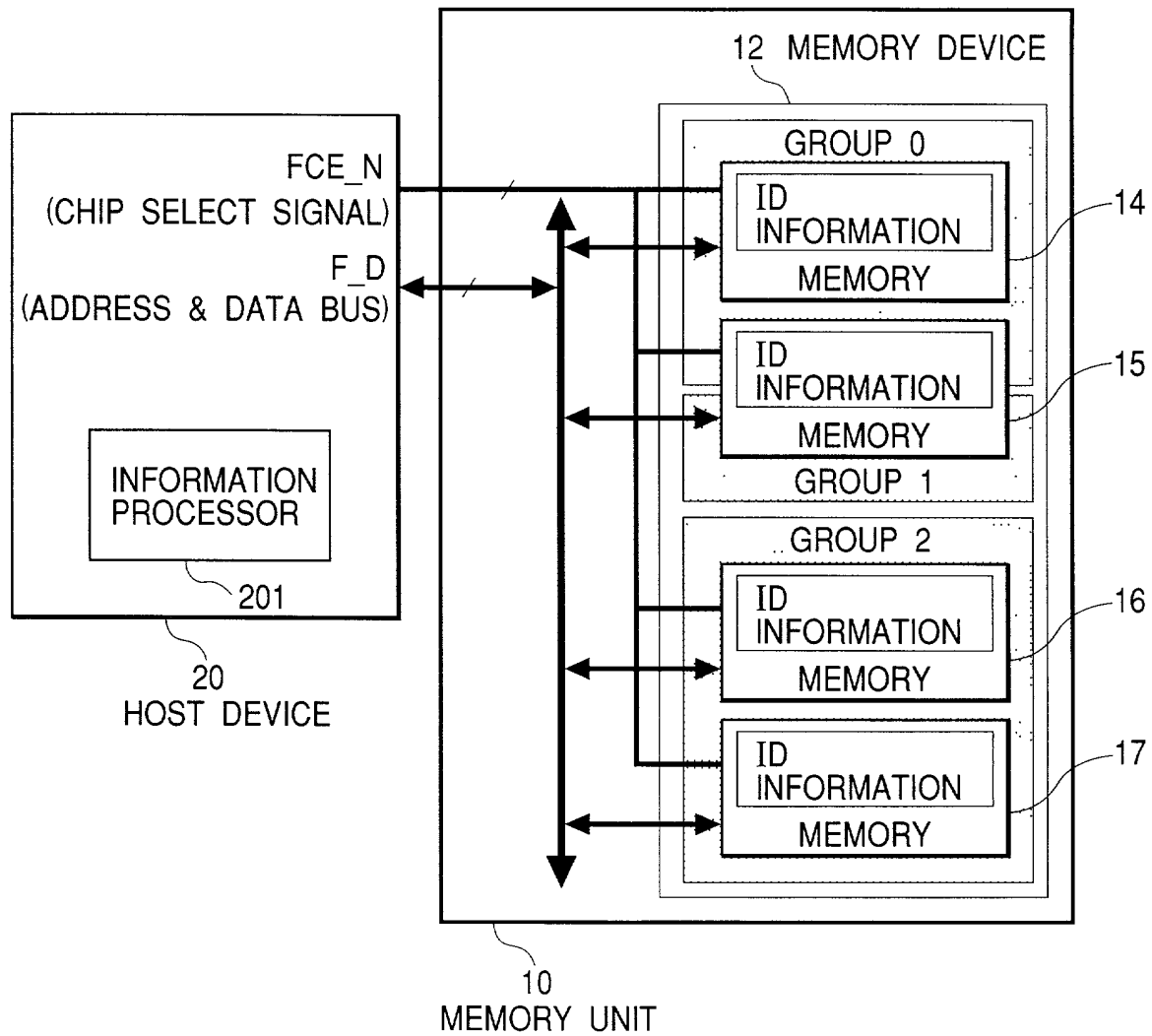


FIG. 19

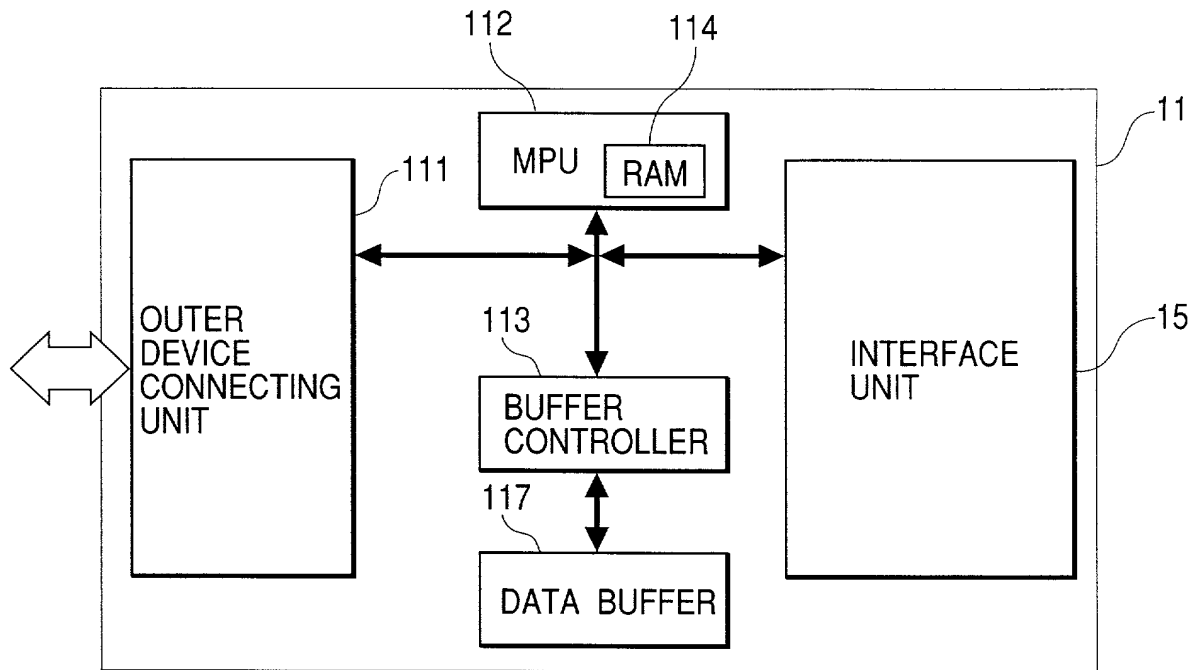


FIG. 20

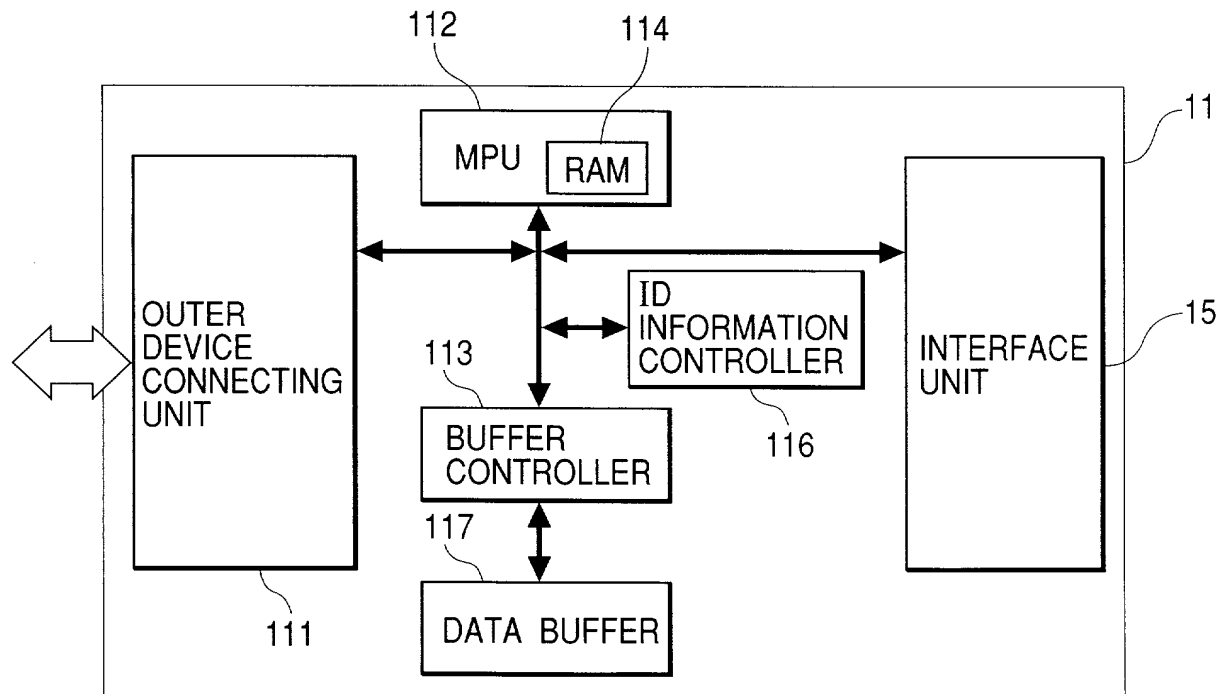


FIG. 21

